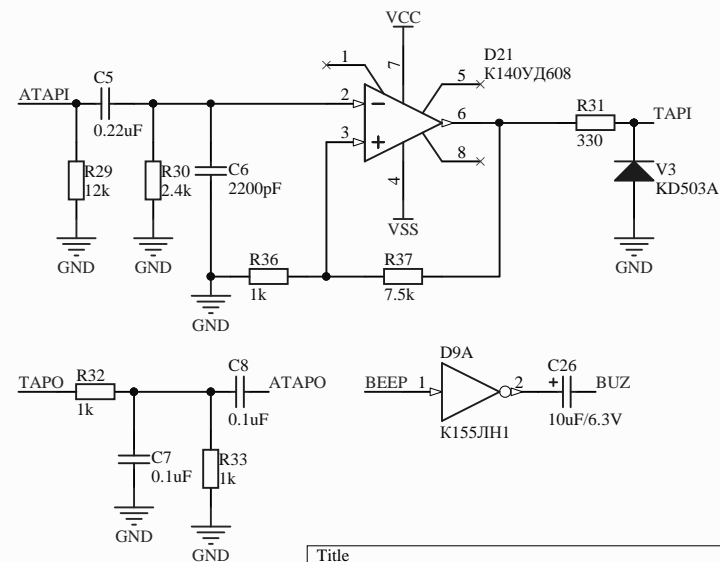
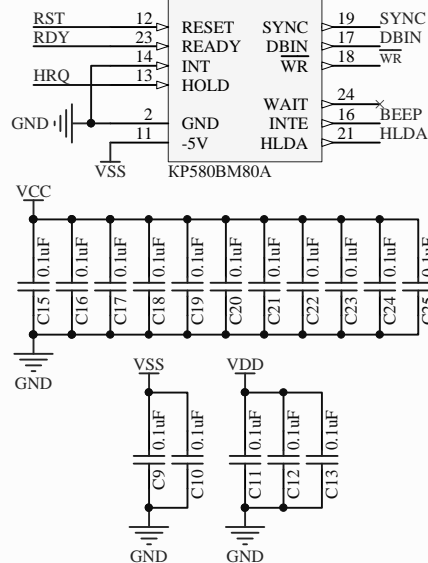
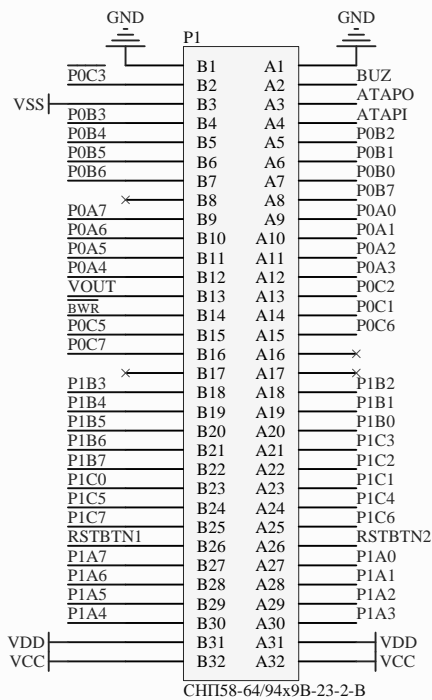
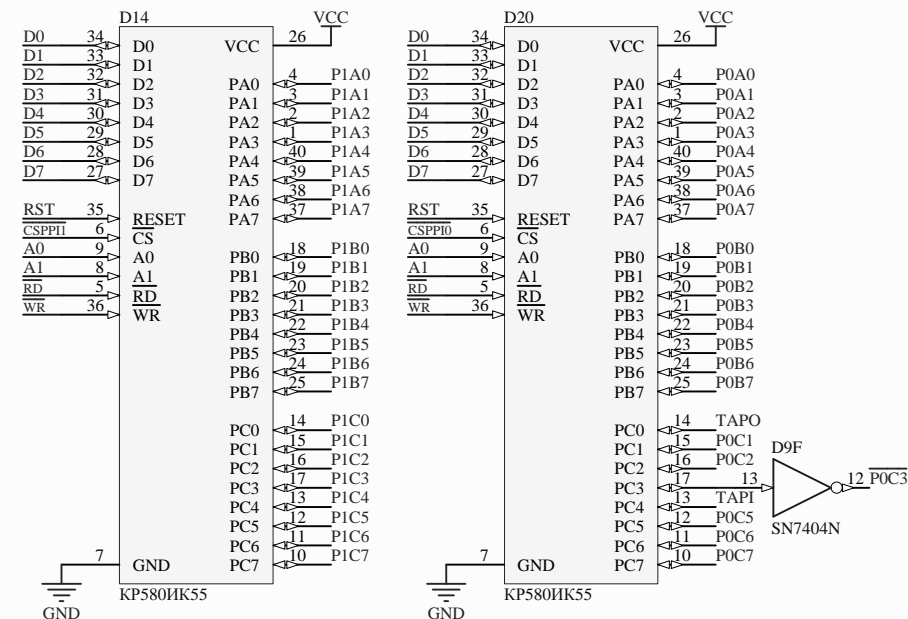
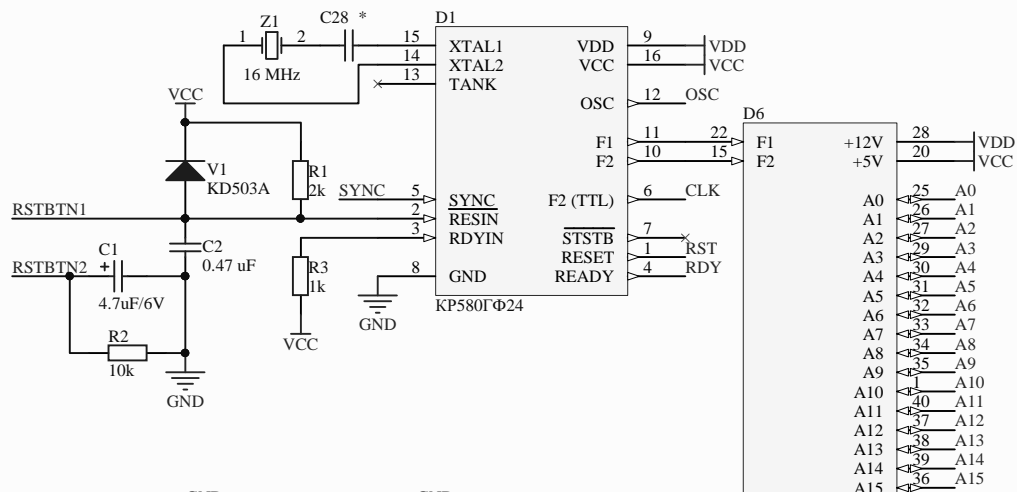


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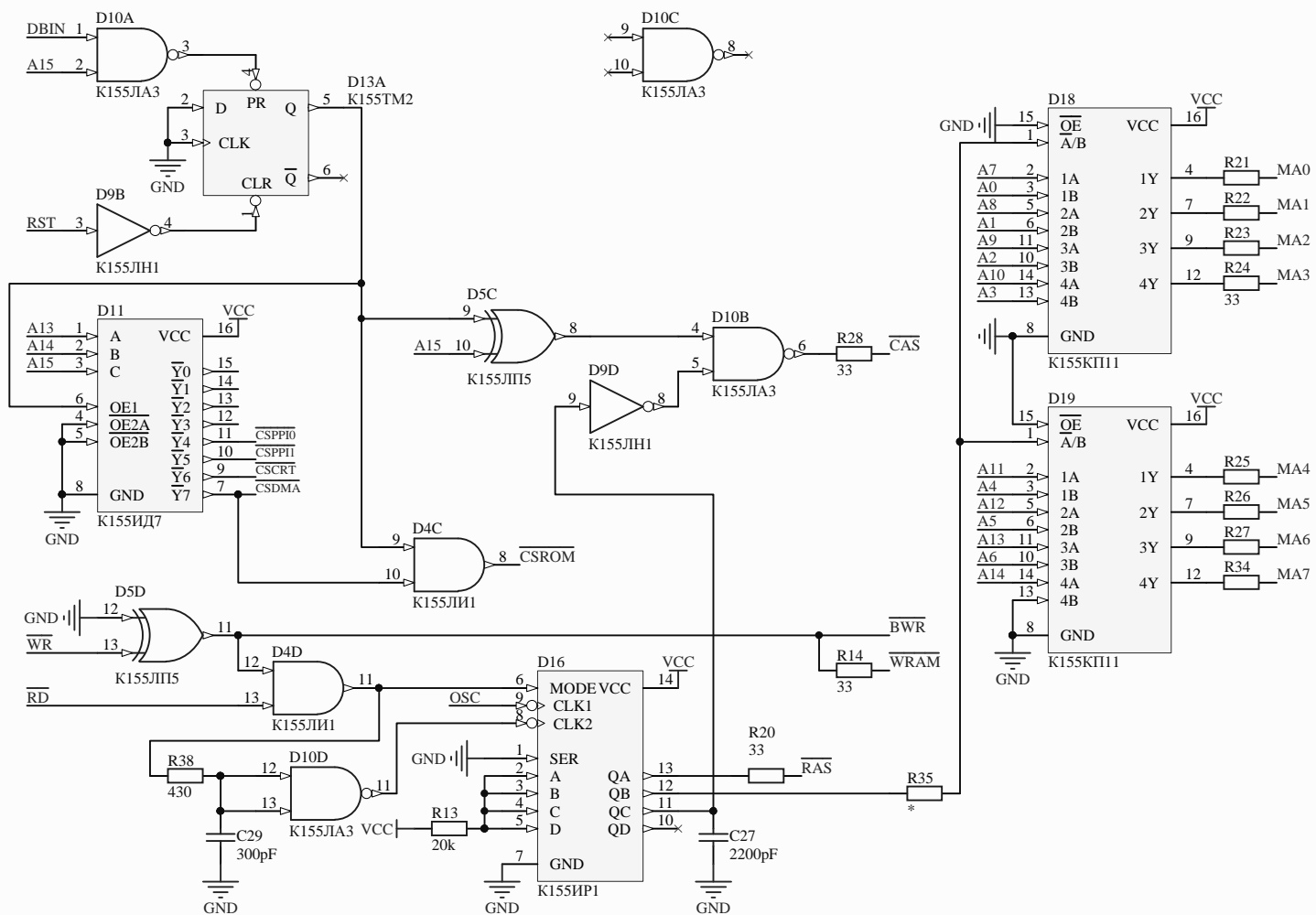
Title		
Radio-86RK. Main oscillator, CPU, PIOs and analog I/O circuitry.		
Size	Number	Revision
A4		Rev. 0
Date:	10.01.2015	Sheet 1 of 4
File:	D:\projects\radio-86rk_cpu_pio.SchDoc	Drawn By: maddev

1

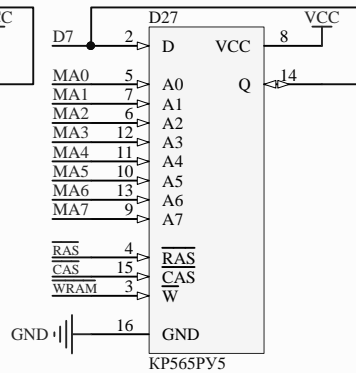
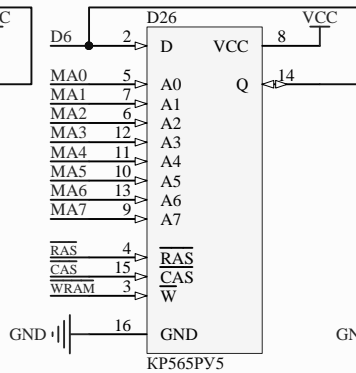
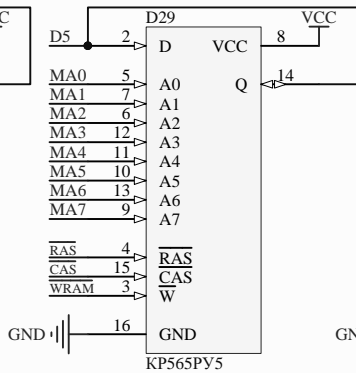
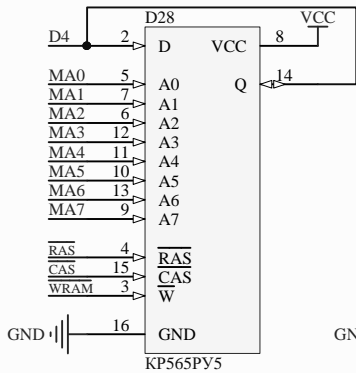
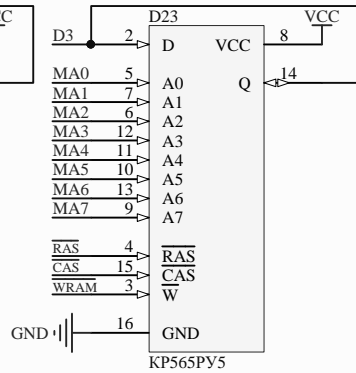
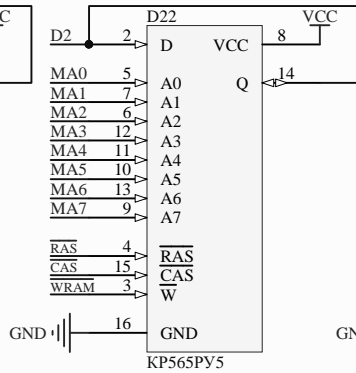
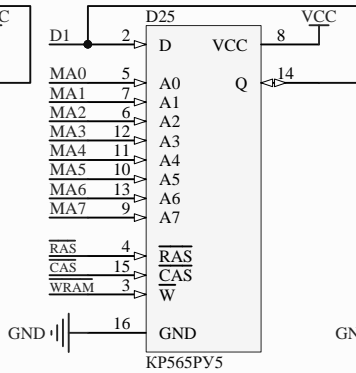
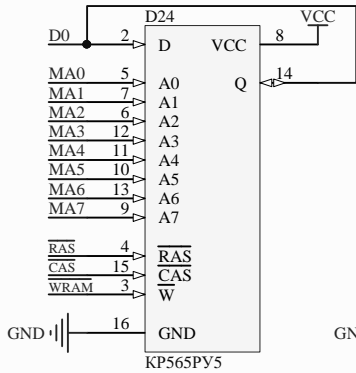
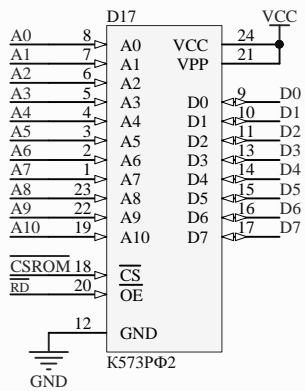
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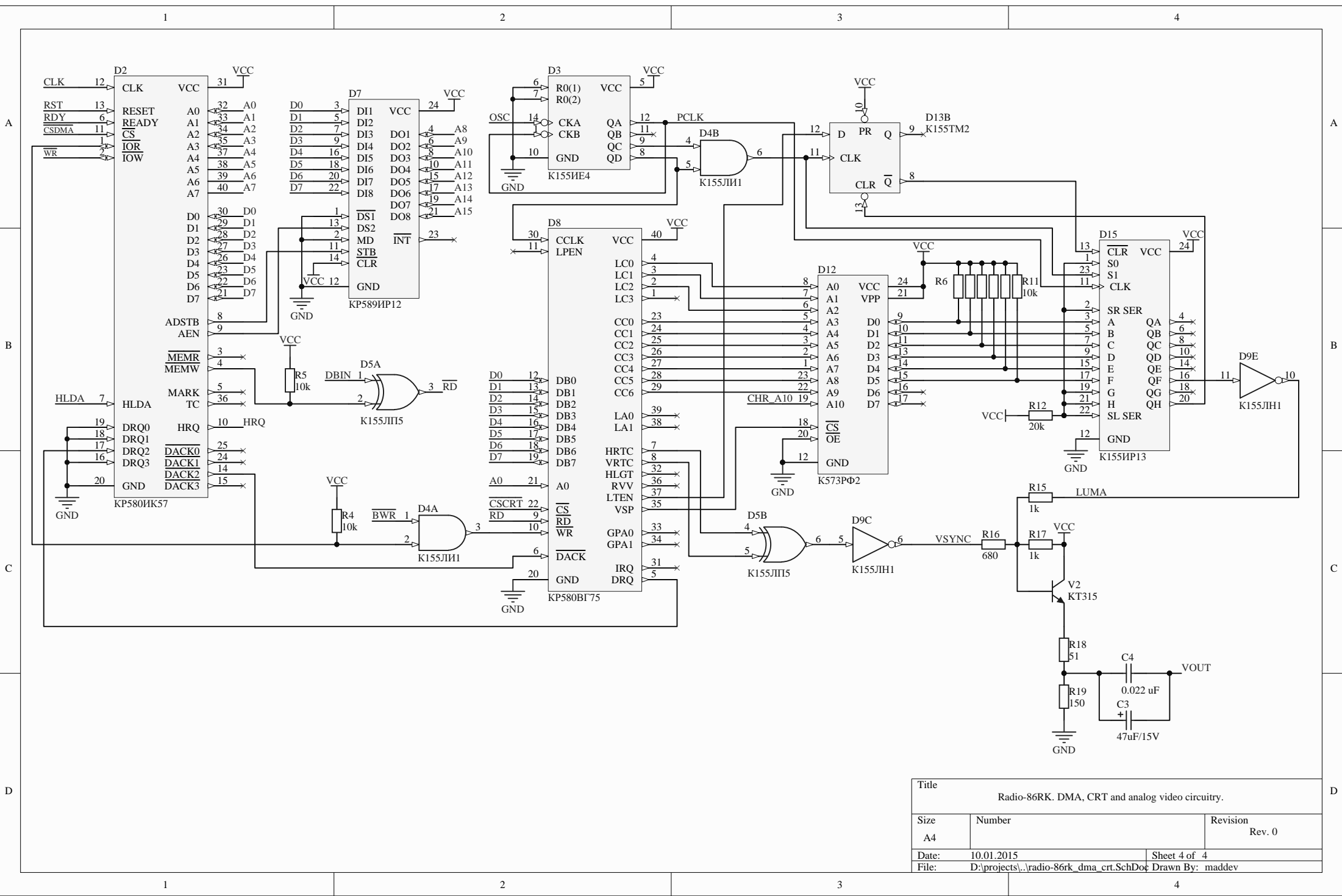
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Title		
Radio-86RK. Address decoding and DRAM control.		
Size	Number	Revision
A4		Rev. 0
Date:	10.01.2015	Sheet 2 of 4
File:	D:\projects\...\radio-86rk_control.SchDoc	Drawn By: maddev



Title		
Radio-86RK. ROM and RAM.		
Size	Number	Revision
A4		Rev. 0
Date:	10.01.2015	Sheet 3 of 4
File:	D:\projects\...\radio-86rk_rom_ram.SchDoc Drawn By: maddev	



Title			Radio-86RK. DMA, CRT and analog video circuitry.
Size	Number	Revision	
A4		Rev. 0	
Date:	10.01.2015	Sheet 4 of 4	
File:	D:\projects\...\radio-86rk_dma crt.SchDoc	Drawn By: maddev	